

B'nded

MOS construction type capacitance element in series with the piezoelectric resonator, one end of the MOS construction type capacitance element is applied with a bias voltage which is the V voltage at an output end or input end of the inverter amplifier and the other end of the MOS construction type capacitance element is supplied with a control voltage that varies within a range whose intermediate value is the V voltage and of a same polarity as the V voltage.

Please amend claim 4 (amended) as follows:

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4. (Twice Amended) A piezoelectric oscillator, wherein, in an inverter oscillator in which a piezoelectric element is connected to an input or output end of an inverter amplifier; and divisional capacitors C1 and C2 are connected between respective ends of the piezoelectric element and the ground, and wherein a MOS construction type capacitance element is inserted between the piezoelectric element and an input end of the inverter amplifier or between the piezoelectric element and an output end of the inverter amplifier; a control voltage Vcont is applied to the terminal on a connection-to-piezoelectric element side of the MOS construction type capacitance element; and, when it is assumed that V represents the voltage that is a direct current bias voltage at the input end or output end of the inverter amplifier and that is applied to one end of the MOS construction type capacitance element, it is arranged that said voltage becomes an intermediate voltage of the control voltage Vcont and of a same polarity as the control voltage Vcont.

REMARKS

Applicant has amended claims 1, 2 and 4. Applicant respectfully submits that the amendments to the claims are supported by the application as originally filed and do not contain any new matter. Accordingly, the Office Action will be discussed in terms of the claims as amended.

The Examiner has rejected claims 1, 2 and 4 under 35 USC 102 as being anticipated by Ochiai et al., stating that Ochiai et al. discloses an integrated piezoelectric oscillator with amplifier 13, resonator 17 and divisional capacitors 15, 18 and an AC voltage is developed on the amplifier input/output terminals while an injection voltage terminal Ti is connected to the other terminal of the MOS capacitor 14 showing a control voltage falling within a range.